

Amendments to the claims:

This listing of claims will replace all prior versions, and listing in the application:

Listing of claims

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Claim 1 (currently amended) A computer motherboard architecture comprising:

a computer motherboard possessing typical components including a CPU, a data bus, a power interface, and an audio input data pathway, said audio input data pathway connecting the audio input of the motherboard to the CPU;

a DSP chip in the audio input data path;

a bridge interfacing between said DSP chip and the bus on the computer motherboard;

a memory in electrical connection to said DSP chip;

a command and control speech engine residing in said memory of said DSP chip;

wherein said DSP is enabled to operate in either command and control mode or continuous speech mode and said DSP serves as the preprocessor of all speech input prior to execution of instructions by the CPU to process the speech input and wherein said speech engine includes a vocabulary of speech terms enabled to be loaded into said memory which are associated with specific instructions or contextual environments, and further wherein said DSP is enabled to be dynamically set by a user in either a continuous speech mode or a command and control mode.

Claims 2-3 (canceled)

Claim 4 (previously presented) A computer motherboard architecture according to claim 1 wherein said audio input data pathway comprises a microphone input, means for

digitizing an audio input data pathway, and a DSP chip, bridge chip communicating with said bus.

Claim 5 (previously presented) A computer motherboard architecture according to claim 1 wherein said DSP chip is operable to convert said audio input into phonemes.

Claim 6 (canceled)

Claim 7 (previously presented) A computer motherboard architecture according to claim 1 wherein said vocabulary of speech terms resides in said memory in electrical connection to said DSP chip.

Claim 8 (previously presented) A computer motherboard architecture according to claim 1 wherein said vocabulary of speech terms is able to be defined by a user, either in a static or active mode.

Claim 9 (previously presented) A computer motherboard architecture according to claim 1 wherein said vocabulary of speech terms is refreshed by the CPU based upon the context of an application running on a host processor.

Claim 10 (previously presented) A computer motherboard architecture according to claim 1 wherein said DSP chip is operable to perform preprocessing for a software-based speech engine residing elsewhere on a computer.

Claim 11 (previously presented) A computer motherboard architecture according to claim 1 wherein said DSP chip is operable to perform menu selection including mobile phone audio functions comprising voice activated dialing, voice control, noise cancellation, and speech to signal conversion.

Claim 12 (previously presented) A computer motherboard architecture according to claim 1 wherein said DSP chip is enabled to perform noise cancellation functions.

Claim 13 (previously presented) A computer motherboard architecture according to claim 1 wherein said DSP chip is enabled to function in a command and control speech mode.

Claim 14 (previously presented) A computer motherboard architecture according to claim 1 wherein said DSP chip is enabled to function in a continuous speech mode.

Claim 15 (previously presented) A computer motherboard architecture according to claim 1 wherein said DSP chip is enabled to function in a mobile phone mode.

Claim 16 (previously presented) A computer motherboard architecture according to claim 1 wherein said DSP is enabled to function in a language translation mode.

Claim 17 (previously presented) A computer motherboard architecture according to claim 1 wherein said computer motherboard is a user-supported computer motherboard.

Claim 18 (previously presented) A computer motherboard architecture according to claim 17 wherein said user-supported computer is a voice activated user-supported computer.

Claim 19 (previously presented) A computer motherboard architecture according to claim 1 wherein said computer motherboard is a portable computer motherboard.

Claim 20 (canceled)

Claim 21 (previously presented) A computer motherboard architecture according to claim 1 wherein said computer motherboard is a desktop computer motherboard.

Claim 22 (canceled)

Claim 23 (previously presented) A computer motherboard architecture according to claim 1 wherein said computer motherboard is a video gaming system computer motherboard.

Claim 24 (previously presented) A computer motherboard architecture according to claim 1 wherein said computer motherboard is a computing and communications device computer motherboard.

Claim 25 (previously presented) A computer motherboard architecture of claim 1 wherein said computer motherboard is a component of a member selected from the group consisting of user supported computers, laptop computer, desktop computers, portable computers and mixtures thereof.

Claims 26-30 ~~(canceled)~~

Claim 31 (previously presented) A computer motherboard architecture according to claim 1 wherein when said DSP is operating in command and control mode said DSP is operable to accommodate full interpreting and processing of said speech without said CPU being utilized.

Claim 32 ~~(canceled)~~

Claim 33 (currently amended) A method of processing speech, the method comprising the steps of:

setting a computer in either command and control mode or continuous speech mode,

inputting speech into an audio input device wherein said audio input device is electrically connected to said computer,

converting said speech from an analog format to a digital signal,  
transmitting said digital signal to a digital signal processor, wherein said digital signal processor is included on a motherboard of said a computer and said digital signal processor is enabled to function as a preprocessor of all speech input,  
analyzing said digital signal with at least said digital signal processor and a speech engine residing in a memory on said motherboard and electrically connected to said digital signal processor,  
transmitting said analyzed digital signal of a computer command to a processor in electrical connection to said digital signal processor and said memory of said computer,  
transmitting said analyzed digital signal of continuous speech to a processor in electrical connection to said digital signal processor and said memory of said computer,  
performing an operation or command representative of said analyzed digital signal by said processor.

Claim 34 (previously presented) The method of claim 33, after said step of analyzing, further comprising the step loading an appropriate vocabulary into said speech engine depending on the context of the operation being performed by a user.

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